



Docket No. 0756-2247

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Osamu NAKAMURA et al.)
Serial No.: 09/748,121) Group Art Unit:
Filed: December 27, 2000) Examiner:
For: SEMICONDUCTOR DEVICE AND)
METHOD FOR FORMING THE SAME)

VERIFICATION OF TRANSLATION

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

I, Izumi Matsukuma, 2-D, 1551, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan, herewith declare:

that I am well acquainted with both the Japanese and English Languages; and

that to the best of my knowledge and belief the following is a true and correct translation of the above referenced U.S. application No. 09/748,121 filed on December 27, 2000.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: this 15 day of May, 2001

Izumi Matsukuma

Name: Izumi Matsukuma